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APPLICANTS

Anthony I-Chih Chou, Fishkill, NY;

Toshiharu Furukawa, Essex Junction, VT;

Patrick R. Varekamp, Croton on Hudson, NY; Jeffrey W. Sleight, Ridgefield, CT;

Akihisa Sekiguchi, Briarcliff Manor, NY;

** CONTINUING DATA *****

None bkr 08/27/04

** FOREIGN APPLICATIONS *****

None bkr 08/27/04

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 11/25/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	NY	17 ✓	20/18	3 ✓
Examiner's Signature <i>B.W. Reenan</i> Initials <i>Ble</i>					

ADDRESS

32074

INTERNATIONAL BUSINESS MACHINES CORPORATION

DEPT. 18G

BLDG. 300-482

2070 ROUTE 52

HOPEWELL JUNCTION, NY

12533

TITLE

METHOD FOR SEPARATELY OPTIMIZING THIN GATE DIELECTRIC OF PMOS AND NMOS TRANSISTORS
WITHIN THE SAME SEMICONDUCTOR CHIP AND DEVICE MANUFACTURED THEREBY

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees
RECEIVED	No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> 1.16 Fees (Filing)
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